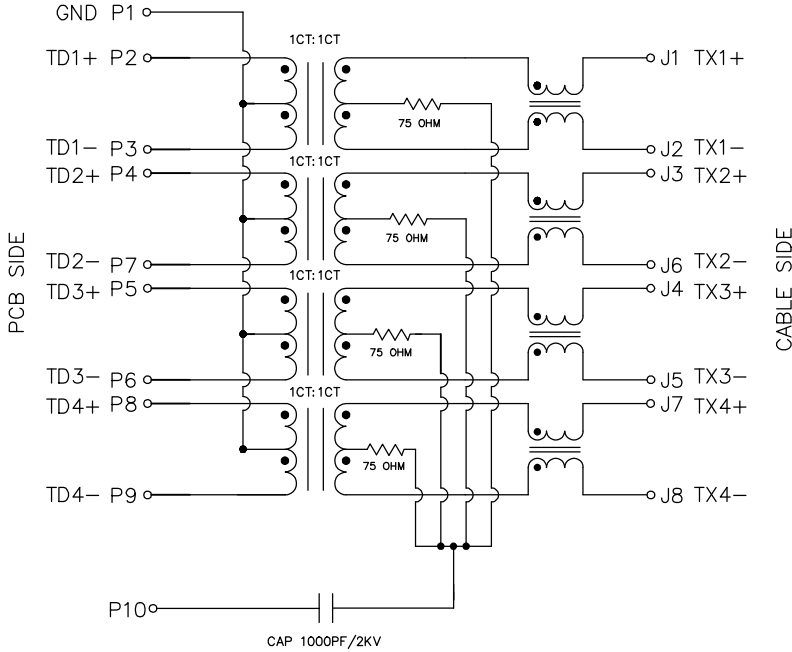


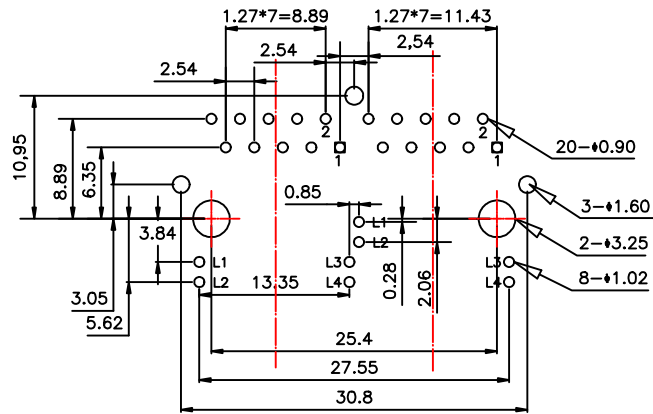
E



B.SCHEMATICS



C:PIN ASSIGNMENT FOR PCB LAYOUT



SUGGESTED PCB PAD LAYOUT
COMPONENT SIDE VIEW

D. ELECTRICAL SPECIFICATIONS @25℃

TRANSMITTER FILTER & RECEIVER FILTER

TYPE : BALANCE LOW PASS 100Ω IMPEDANCE

INSERTION LOSS : 1~125MHz -1.2dB MAX.

RETURN LOSS : 1~30MHz -18dB MIN. LOAD 100Ω
30~60MHz -16dB MIN. LOAD 100Ω
60~80MHz -12dB MIN. LOAD 100Ω
80~100MHz -10dB MIN. LOAD 100Ω

COMMON MODE REJECTION
@1~100 MHz -30dB MIN.

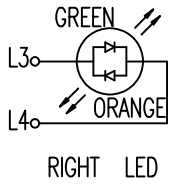
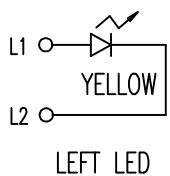
CROSS TALK
@ 1~100MHz -30dB MIN.

INDUCTANCE (OCL) @ 100KHz, 0.1V, 8mA DC BIAS 350 uH MIN.

DCR:1.2 OHM MAX

HIPOT TEST
PHY SIDE(INPUT) TO CABLE SIDE(OUTPUT) : 1500Vrms 60S

E:LED SPECIFICATION



Emitting Color	λp (nm)	Vf @If=20mA	Ir @Vr=5V
GREEN	570	1.7 ~2.6 V	10mA max
ORANGE	605	1.7 ~2.6 V	10mA max
YELLOW	605	1.7 ~2.6 V	10mA max

X. ± 0.40	APPD: LOUIS	 思科飞尔技术（深圳）有限公司 SISCOFERR TECHNOLOGY (SHENZHEN) CO.LTD			
.X ± 0.30					
.XX ± 0.25	CHKD: ZHOU				
.XXX ± 0.20					
X* ± 3'	.X* ± 2'	PART NO: RB2-GA1BE008BAR			
UNITS: mm	DR: LIANG	TITLE: TAB UP 1X2Port 10/100/1000 BASE-T RJ45 JACK Magnetics			
	SHEET: 2 OF 2	REV: A	DWG NO: RB1-0011	REMARK: XXX-XXXX	
	SCALE: 1:1				